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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/759,388	01/15/2004	Yoshinori Fujiwara	MIC-43	7144
1473	7590	03/23/2005	EXAMINER	
FISH & NEAVE IP GROUP ROPES & GRAY LLP 1251 AVENUE OF THE AMERICAS FL C3 NEW YORK, NY 10020-1105			TRAN, MICHAEL THANH	
			ART UNIT	PAPER NUMBER
			2827	

DATE MAILED: 03/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/759,388

Applicant(s)

FUJIWARA, YOSHINORI

Examiner

Michael t. Tran

Art Unit

2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on January 15, 2004 through August 25, 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-33 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 4-15 and 18-33 is/are allowed.
- 6) ☒ Claim(s) 1-3, 16 and 17 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
  - 2) ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 082504.

- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

**MICHAEL TRAN**

## DETAILED ACTION

1. In response to the Communications dated January 15, 2004 through August 25, 2004, claims 1-33 are active in this application.

### ***Foreign Priority***

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a) (d), which papers have been placed of record in the file.

### ***Information Disclosure Statement***

3. The information disclosure statement filed August 25, 2004 has been considered.

### ***Claim Rejections – 35 U.S.C. § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in-

(1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or

(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

5. Claims 1-3 are rejected under 35 U.S.C 102(e) as being anticipated by Kliewer et al. [U.S. Patent #6,781,889].

With respect to claim 1, Kliewer et al. disclose a method of deactivating a word line in a memory circuit, said method comprising: activating a plurality of word lines either one at a time, simultaneously in subpluralities, combination thereof, or all simultaneously, all said word lines being active concurrently; and deactivating one of said word lines, the other of said word lines remaining active. See column 5. In the cited column, Kliewer et al. indicated that the activation and deactivation of a particular word line or groups of word lines depends on whether the control signal indicate to the word line decoder to select or deselect a particular word line or groups of word lines.

With respect to claim 2, Kliewer et al. disclose that a particular word line or groups of word lines can be activated and deactivated.

With respect to claim 3, Kliewer et al. disclose that the deactivating comprises deactivating one of said word lines in response to receiving a signal pulse [CTRLX – see column 5], the other of said word lines remaining active [again, this depends on whether the control signal indicate to the word line decoder to select or deselect a given word line or groups of word lines], other pulses [variation of the CTRLX] of said signal used to activate said word lines.

6. Claim 16 is rejected under 35 U.S.C 102(e) as being anticipated by Kliewer et al. [U.S. Patent #6,781,889].

With respect to claim 16, Kliewer et al. disclose a method of deactivating less than all activated memory word lines in a memory circuit, said method comprising: receiving at least one pulse of a signal [CTRLX – see column 5]; activating a plurality of word lines in response to said receiving at least one pulse; selecting a subplurality of said word lines to be deactivated; receiving a subsequent pulse of said signal; and deactivating said subplurality of word lines in response to said receiving a subsequent pulse. See column 5. In the cited column, Kliewer et al. indicated that the activation and deactivation of a particular word line or groups of word lines depends on whether the control signal indicate to the word line decoder to select or deselect a particular word line or groups of word lines.

7. Claim 17 is rejected under 35 U.S.C 102(e) as being anticipated by Kliewer et al. [U.S. Patent #6,781,889].

With respect to claim 17, Kliewer et al. disclose a method of deactivating a single memory word line among a plurality of activated word lines in a memory circuit, said method comprising: receiving a plurality of address signals; receiving at least one activate signal [CTRLX – see column 5]; activating a plurality of word lines corresponding respectively to said plurality of address signals in response to said receiving at least one activate signal pulse; receiving one of said plurality of address signals after said activating; receiving a deactivate enable signal corresponding to said one address signal; receiving a subsequent activate signal pulse after said activating; and deactivating only said word line corresponding to said one address signal in

response to said receiving a subsequent activate signal pulse. See column 5. In the cited column, Kliwer et al. indicated that the activation and deactivation of a particular word line or groups of word lines depends on whether the control signal indicate to the word line decoder to select or deselect a particular word line or groups of word lines.

***Allowable Subject Matter***

8. Claims 4-15 and 18-33 are allowable over the prior art of record.
9. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to the other elements in the claim) the following:
  - Receiving a subsequent pulse of said signal, said subsequent pulse having the same logical value as said first pulse; and deactivating said word line in response to said receiving a subsequent pulse.
  - Deactivating only said word line in response to receiving said second signal, a fourth signal, and a subsequent pulse of said third signal, said pulse and said subsequent pulse having the same logical value.
  - Generating a sequential series of signal pulses, each said pulse operative to activate a word line and having the same logical value; deactivating only said one selected word line in response to one of said signal pulses.
  - A memory chip coupled to said memory controller, said memory chip comprising an array of memory cells, word lines, address decoder logic, and a latch circuit that outputs a signal indicating activation of a word line in response to receiving a

first signal pulse of an activate signal and that outputs a signal indicating deactivation of said word line in response to receiving a deactivate-enable signal and a subsequent signal pulse of said activate signal.

- Means for deactivating only said word line in response to receiving said second signal, a fourth signal, and a subsequent pulse of said third signal, said subsequent pulse having the same logical value as said pulse.

### ***Conclusion***

10. When responding to the Office action, Applicants are advised to provide the Examiner with line and page numbers of the application and/or references cited to assist the Examiner in the prosecution of this case.

11. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795. The Examiner can normally be reached on Monday-Thursday from 7:30-6:00 P.M.

12. Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (571) 272-1650.



Michael T. Tran  
Art Unit 2827  
March 18, 2005

**MICHAEL TRAN**  
**PRIMARY EXAMINER**